

APPLICATION DATA SHEET

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Application Information: Title: Direct Synthesis Clock Generation Circuits And Methods
Classification: Class: Subclass:
Tech Center:
Drawing Sheets: 7 Drawing to be Published: ____
Docket No: 1-64-CA (P119US)
Type: Utility
Secrecy: None

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Domestic Priority: None CFR §119 Foreign Priority: None Filed on: n/a

Assignee: Cirrus Logic, Inc. 4210 South Industrial Boulevard, Austin, Travis County, Texas 78744